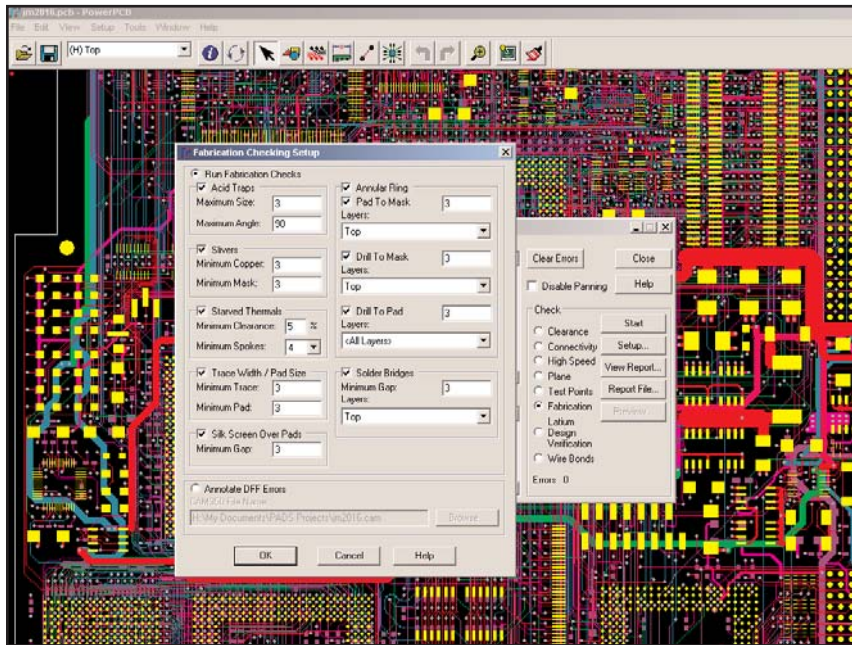


DFF Audit



DFF Audit enables you to avoid fabrication obstacles directly from within PADS® Layout

Overview

In today's demanding market, you must work more effectively and cooperatively with your vendors to avoid revision spins and costly delays in production. The goals of optimized quality, reduced cycle time, and cost control have become issues for everyone in the product development process. That is why designing for fabrication (DFF) has become so critical when designing a printed circuit board. Ensuring your designs can be manufactured — before they are released to fabrication — will significantly reduce costs and time-to-market.

DFF Audit was created to bring fabrication constraints and requirements to the attention of the designer. By adding this functionality to PADS® Layout you can design boards knowing that they can be successfully fabricated. By working proactively, in collaboration with fabrication, you can create a seamless transition from design to manufacturing. This gives you the peace of mind that your designs will be easily and successfully manufactured right from the start.

Major product features

Enables you to produce quality designs ready for fabrication

Finds, analyzes, and amends manufacturing violations while you design

Automatically flags acid traps, starved thermals, slivers, and more

Removes unnecessary design revisions from your process

Directly impacts your bottom line by supplying quality layouts to fabrication

Designing for Fabrication

With DFF Audit, you can ensure that your designs do not conceal any manufacturing rule violations. DFF Audit performs all the essential bare-board analysis checks. Built on state-of-the-art Latium™ architecture, DFF analysis takes only a few minutes to run, and is completed with precision.

Locate, identify, and instantly amend all violations before investing in hard tooling. DFF Audit will automatically check for acid traps, mask slivers, copper slivers, starved thermals, spacing, clearance, and more. By making sure that the solder-mask data are generated using proper clearances, you can fix potential acid traps and ensure that there are no potential solder bridge conditions. This alleviates the pressure in any fabrication facility.

Analysis results are graphically displayed in a simple, easy-to-follow format that enables violations to be viewed immediately and potential problems to be corrected in an instant. Simply go into your design database and correct the issue accordingly. By directly updating the source, you are ensuring the integrity of your design database at all times.

The Importance of DFF

Performing DFF analysis during the design process keeps you in control of your database and the changes being made. Without DFF Analysis, fabricators must manipulate your design files in order to produce bare boards. For instance, if your fabricator detects an acid trap, he or she might simply drop copper into the crevice, whereas you would prefer rerouting the trace and removing the chance of an acid trap altogether.

Waiting to address issues in fabrication also means that your design database does not get updated properly with vital changes that the fabricator makes. As a result, the integrity of your design is compromised. If the source files aren't updated, your fab house might miss those issues during a respin and faulty boards might result.

Summary

DFF Audit gives you the ability to design for manufacturing accuracy and excellence. No more passing your designs to fabrication only to have them returned to you for alterations and explanations. Design with confidence and knowledge. DFF Audit will detect, define, and eliminate manufacturing violations. These violations, if not detected early in the design process, can put an immediate stop to production - resulting in costly scrap, missed market windows, and excessive expense. With DFF Audit, you can ensure the integrity of your designs before your fabrication facility builds them. This is critical to the success of today's electronic products.

PADS Layout Options

- Advanced Packaging Toolkit
- Advanced Rule Set (ARS)
- Analog Toolkit
- Assembly Variants
- Cluster Placement
- Database Viewer
- DFF Audit
- DFT Audit
- PADS Router interactive route editor
- PADS Router HSD
- IDF Link
- Physical Design Reuse (PDR)

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